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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/611,948	07/03/2003	Hisashi Ishikawa	00862.023127.	5415
****	7590 05/16/2007 CELLA HARPER & S	EXAMINER		
30 ROCKEFELLER PLAZA			VO, QUANG N	
NEW YORK, NY 10112			ART UNIT	PAPER NUMBER
			2625	
			MAIL DATE	DELIVERY MODE
			05/16/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)			
	10/611,948	ISHIKAWA, HISASHI			
Office Action Summary	Examiner	Art Unit			
	Quang N. Vo	2625			
The MAILING DATE of this communication appeariod for Reply	ppears on the cover sheet w	ith the correspondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING I Extensions of time may be available under the provisions of 37 CFR 1 after.SIX (6) MONTHS from the mailing date of this communication.  If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNI .136(a). In no event, however, may a d will apply and will expire SIX (6) MOI te, cause the application to become A	CATION. reply be timely filed  NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on <u>03</u> .	July 2003.				
2a) ☐ This action is <b>FINAL</b> . 2b) ☐ Th	This action is <b>FINAL</b> . 2b) ☐ This action is non-final.				
3) Since this application is in condition for allow	·				
closed in accordance with the practice under	Ex parte Quayle, 1935 C.L	J. 11, 453 O.G. 213.			
Disposition of Claims					
4) ⊠ Claim(s) <u>1-61</u> is/are pending in the applicatio 4a) Of the above claim(s) is/are withdres 5) □ Claim(s) is/are allowed. 6) □ Claim(s) is/are rejected. 7) □ Claim(s) is/are objected to. 8) ⊠ Claim(s) <u>1-61</u> are subject to restriction and/or	awn from consideration.				
Application Papers					
9) The specification is objected to by the Examin		by the Evenines			
10) The drawing(s) filed on is/are: a) ac Applicant may not request that any objection to the					
Replacement drawing sheet(s) including the corre	- · ·				
11) The oath or declaration is objected to by the E	Examiner. Note the attache	d Office Action or form PTO-152.			
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of:  1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the pri application from the International Bures * See the attached detailed Office action for a list	nts have been received. nts have been received in A ority documents have beer au (PCT Rule 17.2(a)).	Application No  received in this National Stage			
Attachment(s)  1)  Notice of References Cited (PTO-892)		Summary (PTO-413)			
Notice of Draftsperson's Patent Drawing Review (PTO-948)     Information Disclosure Statement(s) (PTO/SB/08)     Paper No(s)/Mail Date		(s)/Mail Date Informal Patent Application 			

## **DETAILED ACTION**

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## Election/Restrictions

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 1-7, 20-26, 39 are drawn to an apparatus for quantizing input image data and reducing the impact of an arithmetic error by said error diffusion component on a next input image data.
- II. Claims 8, 27, 40 are drawn to an apparatus for a bit extension component that bit –extends image data of a pixel of interest and a correction component that corrects the bit-extended image data and storage component that stores a decimal portion of the correction value, which is to be connected to a lower bit side of the next image data in bit extension processing by said bit extension component.
- III. Claims 9-19, 28-38, 41-49 are drawn to an apparatus for an addition component that adds an integral portion of the held value to the input image data; and a bit connection component that connects a decimal portion of the held value to a lower bit side of the image data with the integral portion added and outputs the image data to said quantization component.
- IV. Claims 50-61 are drawn to an image processing method of executing error diffusion processing for n-valued image data to convert the n-valued image data into m-valued image data (2.ltoreq.m<n) to form a pseudo

halftone image, comprising the steps of: inputting the n-valued image data; adding a quantization error accumulated by preceding error diffusion to the input n-valued image data; generating noise; determining a sign of the noise; adding the noise with the determined sign to the n-valued image data with the quantization error added; converting the n-valued image data with the noise added into m-valued image data; inversely quantizing the

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The inventions are distinct, each from other because of the following reasons:

m-valued data into a quantization representative value.

Inventions are related as <u>subcombinations disclosed as usable</u>

<u>together in a single combination</u>. The subcombinations are distinct from each other if at least one subcombination is separately usable. In the instant case, each invention has separate utility such as operating either independently or in combination with other subcombinations according to the particular claimed limitations which characterize the invention, without requiring the particular limitations which characterize the other invention(s), as indicated above. See MPEP paragraph 806.05(d).

Because these inventions are distinct for the reason given above, requiring separate consideration and search, restriction for examination purposes as indicated is proper.

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Should applicant traverse on the ground that the inventions are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the inventions to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103 of the other inventions.

Applicant is advised that the response to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quang N. Vo whose telephone number is 5712701121. The examiner can normally be reached on 7:30AM-5:00PM Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Twyler M. Lamb can be reached on 5712727406. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Quang N. Vo

Quangvo

5/7/07

Patent Examiner

SUPERVISORY PATENT EXAMINER